



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yamazaki, et al.                      Art Unit : 2815  
Serial No. : 09/736,724                      Examiner : Paul E. Brock  
Filed : December 13, 2000                      Confirmation No.: 8339  
Title : INSULATED GATE SEMICONDUCTOR DEVICE AND METHOD OF  
MANUFACTURING THE SAME

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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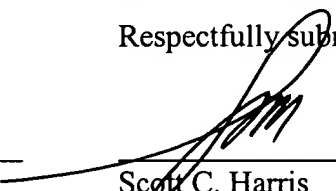
**ASSOCIATE POWER OF ATTORNEY**

Please recognize John F. Hayden, Reg. No. 37,640; James E. Mrose, Reg. No. 33,264; Phyllis K. Kristal, Reg. No. 38,524; W. Karl Renner, Reg. No. 41,265; Gregory A. Walters, Reg. No. 41,366; Diana DiBerardino, Reg. No. 45,653; Joseph F. Key, Reg. No. 44,827; Harold H. Fox, Reg. No. 41,498; Scott R. Boalick, Reg. No. 42,337; Mark E. Wadzyk, Reg. No. 45,187; Benjamin D. Driscoll, Reg. No. 41,571; Ruffin B. Cordell, Reg. No. 33,487; Michael McKeon, Reg. No. 37,888; Linda Liu Kordziel, Reg. No. 39,732; Lauren A. Degnan, Reg. No. 40,584; Joseph V. Colaianni, Jr., Reg. No. 39,948; William G. Hughes, Jr., Reg. No. 46,112; William E. Booth, Reg. No. 28,933; John W. Freeman, Reg. No. 29,066; Timothy A. French, Reg. No. 30,175; G. Roger Lee, Reg. No. 28,963; John B. Pegram, Reg. No. 25,198; Thomas A. Rozylowicz, Reg. No. 50,620; Kevin Greene, Reg. No. 46,031; Barbara V. Benoit, Reg. No. 54,777; Brian T. Racilla, Reg. No. 54,382 and John E. Mauk, Reg. No. 54,579, as my associate attorneys/agents in the above application, with full powers.

Please direct all future official communications to Customer No. 26171.

Respectfully submitted,

Date: 10/25/03

  
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Scott C. Harris  
Reg. No. 32,030

Fish & Richardson P.C.  
4350 La Jolla Village Drive, Suite 500  
San Diego, CA 92122  
Telephone: (858) 678-5070/Facsimile: (858) 678-5099